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THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Mathew et al.

Title: ADDER CIRCUIT WITH SENSE AMPLIFIER MULTIPLEXER FRONT-END

Docket No.: 884.A76US1

Serial No.: 10/728,127

Filed: December 4, 2003

Due Date: N/A

Examiner: Unknown

Group Art Unit: 2813

MS Amendment

Commissioner for Patents

P.O. Box 1450

Alexandria, VA 22313-1450

We are transmitting herewith the following attached items (as indicated with an "X"):

- ☒ Return postcard.
- ☒ Communication Concerning Related Applications (1 pg.).
- ☒ Information Disclosure Statement (2 pgs.), Form 1449 (1 pg.), and copies of 3 cited documents.

Please consider this a **PETITION FOR EXTENSION OF TIME** for sufficient number of months to enter these papers and please charge any additional fees or credit overpayment to Deposit Account No. 19-0743.

SCHWEGMAN, LUNDBERG, WOESSNER & KLUTH, P.A.

Customer Number 21186

By: Daniel J. Kluth

Atty: Daniel J. Kluth

Reg. No. 32,146

CERTIFICATE UNDER 37 CFR 1.8: The undersigned hereby certifies that this correspondence is being deposited with the United States Postal Service with sufficient postage as first class mail, in an envelope addressed to: MS Amendment, Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450, on this 2nd day of September, 2005.

Name

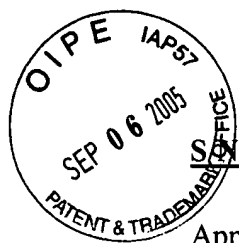
Amy Moriarty

Signature

[Signature]

SCHWEGMAN, LUNDBERG, WOESSNER & KLUTH, P.A.

(GENERAL)



Serial 10/728,127

PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Sanu K. Mathew et al. Examiner: Unknown
Serial No.: 10/728,127 Group Art Unit: 2813
Filed: December 4, 2003 Docket: 884.A76US1
Title: ADDER CIRCUIT WITH SENSE AMPLIFIER MULTIPLEXER FRONT-END

COMMUNICATION CONCERNING RELATED APPLICATION(S)

MS Amendment
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Applicants would like to bring to the Examiner's attention the following related application(s) in the above-identified patent application:

<u>Serial/Patent No.</u>	<u>Filing Date/Issue Date</u>	<u>Attorney Docket</u>	<u>Title</u>
09/967240	September 28, 2001	884.448US1	HIGH-PERFORMANCE ADDER

Continuations and divisionals may be later filed on the cases listed above, or cited to the Examiner in any previous Communication Concerning Related Applications. Applicants request that the Examiner review all continuations and divisionals of the above-listed or previously-cited patent applications before allowing the claims of the present patent application.

Respectfully submitted,

SANU K. MATHEW ET AL.

By Applicants' Representatives,

SCHWEGMAN, LUNDBERG, WOESSNER & KLUTH, P.A.
P.O. Box 2938
Minneapolis, MN 55402
(612) 373-6969

Date

Sep 2, 2005

By

Daniel J. Kluth
Daniel J. Kluth
Reg. No. 32,146

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Name

Amy Moriarty

Signature

[Signature]



S/N 10/728,127

PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant:	Sanu K. Mathew et al.	Examiner:	Unknown
Serial No.:	10/728,127	Group Art Unit:	2813
Filed:	December 04, 2003	Docket:	884.A76US1
Title:	ADDER CIRCUIT WITH SENSE AMPLIFIER MULTIPLEXER FRONT-END		
Assignee:	Intel Corporation	Customer Number:	21186

INFORMATION DISCLOSURE STATEMENT

MS Amendment
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

In compliance with the duty imposed by 37 C.F.R. § 1.56, and in accordance with 37 C.F.R. §§ 1.97 *et. seq.*, the enclosed materials are brought to the attention of the Examiner for consideration in connection with the above-identified patent application. Applicants respectfully request that this Information Disclosure Statement be entered and the documents listed on the attached Form 1449 be considered by the Examiner and made of record. Pursuant to the provisions of MPEP 609, Applicants request that a copy of the 1449 form, initialed as being considered by the Examiner, be returned to the Applicants with the next official communication.

Pursuant to 37 C.F.R. §1.97(b), it is believed that no fee or statement is required with the Information Disclosure Statement. However, if an Office Action on the merits has been mailed, the Commissioner is hereby authorized to charge the required fees to Deposit Account No. 19-0743 in order to have this Information Disclosure Statement considered.

INFORMATION DISCLOSURE STATEMENT

Customer No.: 21186

Serial No :10/728,127

Filing Date: December 04, 2003

Title: ADDER CIRCUIT WITH SENSE AMPLIFIER MULTIPLEXER FRONT-END

Assignee: Intel Corporation

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Dkt: 884.A76US1 (INTEL)

The Examiner is invited to contact the Applicants' Representative at the below-listed telephone number if there are any questions regarding this communication.

Pursuant to 37 C.F.R. 1.98(a)(2), Applicant believes that copies of cited U.S. Patents and Published Applications are no longer required to be provided to the Office. Notification of this change was provided in the United States Patent and Trademark Office OG Notices dated October 12, 2004. Thus, Applicant has not included copies of any US Patents or Published Applications cited with this submission. Should the Office require copies to be provided, Applicant respectfully requests that notice of such requirement be directed to Applicant's below-signed representative. Applicant acknowledges the requirement to submit copies of foreign patent documents and non-patent literature in accordance with 37 C.F.R. 1.98(a)(2).

Respectfully submitted,

SANU K. MATHEW ET AL.

By their Representatives,

SCHWEGMAN, LUNDBERG, WOESSNER & KLUTH, P.A.

Attorneys for Intel Corporation

P.O. Box 2938

Minneapolis, MN 55402

(612) 373-6969

Date

Sep. 2, 2005

By

Daniel J. Kluth

Daniel J. Kluth

Reg. No. 32,146

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Name

Amy Moriarty

Signature

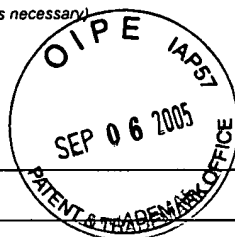
[Signature]

Under the Paperwork Reduction Act of 1995, no persons are required to respond to a collection of information unless it contains a valid OMB control number.

Substitute for form 1449A/PTO

**INFORMATION DISCLOSURE
STATEMENT BY APPLICANT**

(Use as many sheets as necessary)



Complete if Known

Application Number	10/728,127
Filing Date	December 4, 2003
First Named Inventor	Mathew, Sanu
Group Art Unit	2813
Examiner Name	Unknown

Sheet 1 of 1

Attorney Docket No: 884.A76US1

US PATENT DOCUMENTS

Examiner Initial *	USP Document Number	Publication Date	Name of Patentee or Applicant of cited Document	Filing Date If Appropriate
	US-3,553,446	01/05/1971	Kruiy, J. F.	08/04/1966
	US-3,993,891	11/23/1976	Beck, G. R., et al.	07/03/1975
	US-5,047,976	09/10/1991	Goto, G. , et al.	02/22/1991
	US-5,257,218	10/26/1993	Poon, J. T.	02/03/1993
	US-5,327,369	07/05/1994	Ashkenazi, Y.	03/31/1993
	US-5,450,560	09/12/1995	Bridges, J. T., et al.	12/21/1992
	US-5,701,504	12/23/1997	Timko, M. A.	12/28/1994
	US-6,269,386	07/31/2001	Siers, S. E., et al.	10/14/1998
	US-6,496,846	12/17/2002	Bradley, D. H.	07/13/1999
	US-6,782,406	08/24/2004	Koshy, K. J.	06/07/2001

OTHER DOCUMENTS -- NON PATENT LITERATURE DOCUMENTS

Examiner Initials*	Cite No ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
		CHENG, K. , et al., "The Improvement of Conditional Sum Adder for Low Power Applications", <u>IEEE</u> , (1998),131-134	
		JIEN-CHUNG, L. , "A Fast Binary Adder With Conditional Carry Generation", <u>IEEE Transactions on Computers</u> , 46 (2), (Feb. 1997),248-253	
		WOO, RAMCHAN , et al., "A 670ps, 64bit Dynamic Low-Power Adder Design", <u>IEEE International Symposium on Circuits and Systems</u> , Geneva, Switzerland,(May 28-31, 2000),pp I-28-I31	

EXAMINER**DATE CONSIDERED**

Substitute Disclosure Statement Form (PTO-1449)

* EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 809. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant. 1 Applicant's unique citation designation number (optional) 2 Applicant is to place a check mark here if English language Translation is attached